UNISONIC TECHNOLOGIES CO., LTD

5N60 Power MOSFET

5A, 600V N-CHANNEL **POWER MOSFET**

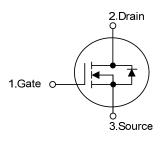
DESCRIPTION

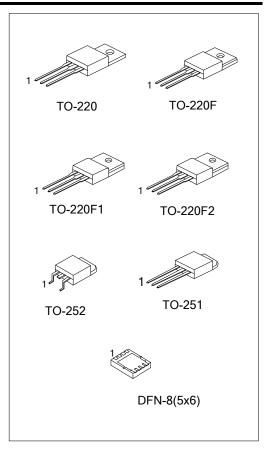
The UTC 5N60 is a high voltage power MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

FEATURES

- * $R_{DS(ON)} = 2.2\Omega @V_{GS} = 10 \text{ V}$
- * Ultra Low Gate Charge (Typical 15 nC)
- * Low Reverse Transfer Capacitance (C_{RSS} = Typical 6.5 pF)
- * Fast Switching Capability
- * Avalanche Energy Specified
- * Improved dv/dt Capability, High Ruggedness

SYMBOL

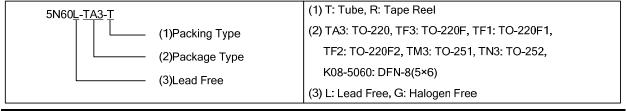




ORDERING INFORMATION

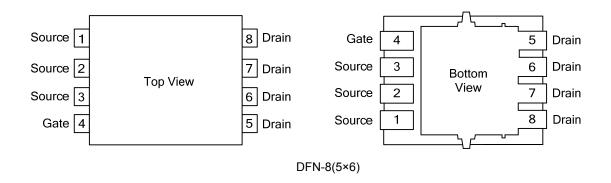
Ordering Number		Package	Pin Assignment							Packing	
Lead Free	Halogen Free	Fackage	1	2	3	4	5	6	7	8	Packing
5N60L-TA3-T	5N60G-TA3-T	TO-220	G	D	S	-	-	-	-	·	Tube
5N60L-TF1-T	5N60G-TF1-T	TO-220F1	G	D	S	-	-	-	-	ı	Tube
5N60L-TF2-T	5N60G-TF2-T	TO-220F2	G	D	S	-	-	-	-	·	Tube
5N60L-TF3-T	5N60G-TF3-T	TO-220F	G	D	S	-	-	-	-	ı	Tube
5N60L-TM3-T	5N60G-TM3-T	TO-251	G	D	S	-	-	-	-	ı	Tube
5N60L-TN3-T	5N60G-TN3-T	TO-252	G	D	S	-	-	-	-	·	Tube
5N60L-TN3-R	5N60G-TN3-R	TO-252	G	D	S	-	-	-	-	-	Tape Reel
5N60L-K08-5060-R	5N60G-K08-5060-R	DFN-8(5×6)	S	S	S	G	D	D	D	D	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source



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■ PIN CONFIGURATION



■ ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V _{DSS} 600		V
Gate-Source Voltage		V_{GSS}	±30	V
Avalanche Current (Note 2)		I_{AR}	5	Α
Continuous Drain Current		I_{D}	5	Α
Pulsed Drain Current (Note 2)		I_{DM}	20	Α
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	210	
	Repetitive (Note 2)	E_{AR}	10	mJ
Peak Diode Recovery dv/dt (N	lote 4)	dv/dt	4.5	V/ns
Power Dissipation	TO-220		100	
	TO-220F/TO-220F1		36	
	TO-220F2	P_{D}	38	W
	TO-251 / TO-252		54	
	DFN-8(5×6)		28	
Junction Temperature		TJ	+150	°C
Operation Temperature		T_OPR	-55 ~ +150	Ĵ
Storage Temperature		T_{STG}	-55 ~ +150	°C

- Note: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

 Absolute maximum ratings are stress ratings only and functional device operation is not implied.
 - 2. Pulse width limited by $T_{\mathsf{J}(\mathsf{MAX})}$
 - 3. L = 16.8mH, I_{AS} = 5A, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25°C
 - 4. $I_{SD} \le 5A$, di/dt $\le 200A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25^{\circ}C$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-220F TO-220F1/ TO-220F2		62.5	°C/W
	TO-251 / TO-252	$ heta_{JA}$	160	
	DFN-8(5×6)		75	
	TO-220		1.25	
Junction to Case	TO-220F/TO-220F1		3.47	
	TO-220F2	θ_{JC}	3.28	°C/W
	TO-251 / TO-252		2.3	
	DFN-8(5×6)		4.46	

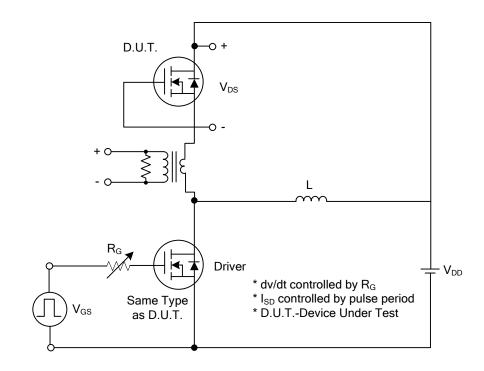
■ **ELECTRICAL CHARACTERISTICS** (T_C = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT		
OFF CHARACTERISTICS									
Drain-Source Breakdown Voltage		BV _{DSS}	$V_{GS} = 0V, I_D = 250\mu A$	600			٧		
Drain-Source Leakage Current		I _{DSS}	$V_{DS} = 600 V, V_{GS} = 0 V$			1	μΑ		
Cata Cauraa I aaka sa Currant	Forward	,	$V_{GS} = 30V, V_{DS} = 0V$			100			
Gate-Source Leakage Current	Reverse	I_{GSS}	$V_{GS} = -30V, V_{DS} = 0V$			-100	nA		
Breakdown Voltage Temperature Coefficient		$\triangle BV_{DSS}/\triangle T_{J}$	I _D =250μA, Referenced to 25°C		0.6		V/°C		
ON CHARACTERISTICS									
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	2.0		4.0	V		
Static Drain-Source On-State Res	istance	R _{DS(ON)}	$V_{GS} = 10V, I_D = 2.5A$		1.8	2.2	Ω		
DYNAMIC CHARACTERISTICS									
Input Capacitance		C _{ISS}	-\/ - 25\/ \/ - 0\/		515	670	pF		
Output Capacitance		Coss	$V_{DS} = 25V, V_{GS} = 0V,$ f = 1.0MHz		55	72	pF		
Reverse Transfer Capacitance		C_{RSS}	I = 1.0WIHZ		6.5	8.5	pF		
SWITCHING CHARACTERISTIC	S								
Turn-On Delay Time		$t_{D(ON)}$			10	30	ns		
Turn-On Rise Time		t _R	$V_{DD} = 300V, I_{D} = 5A,$		42	90	ns		
Turn-Off Delay Time		t _{D(OFF)}	$R_G = 25\Omega$ (Note 1, 2)		38	85	ns		
Turn-Off Fall Time		t_{F}			46	100	ns		
Total Gate Charge		Q_G ,	V _{DS} = 480 V, I _D = 5A,		15	19	nC		
Gate-Source Charge		()00	V _{DS} = 460 V, I _D = 5A, V _{GS} = 10 V (Note 1, 2)		2.5		nC		
Gate-Drain Charge		Q_GD	VGS - 10 V (NOte 1, 2)		6.6		nC		
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS									
Drain-Source Diode Forward Volta	age	V _{SD}	$V_{GS} = 0 \text{ V}, I_{S} = 5\text{A}$			1.4	V		
Maximum Continuous Drain-Source Diode						5	Α		
Forward Current		Is				5	А		
Maximum Pulsed Drain-Source Diode		l lau				20	Α		
Forward Current		I _{SM}				20	^		
Reverse Recovery Time		t _{rr}	$V_{GS} = 0 \text{ V}, I_{S} = 5A,$		300		ns		
Reverse Recovery Charge		Q_{RR}	d _{IF} / dt = 100 A/μs (Note 1)		2.2		μC		

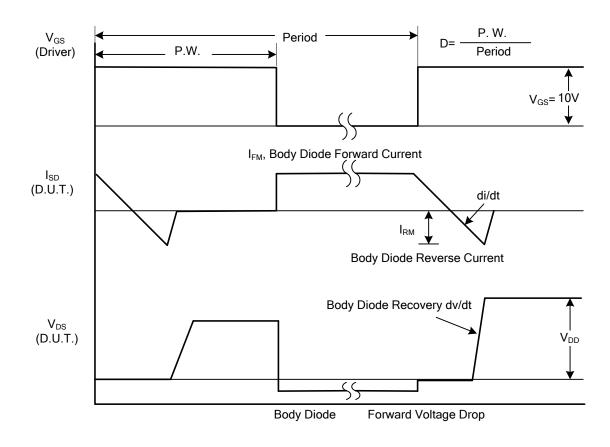
Note: 1. Pulse Test: Pulse width ≤ 300µs, Duty cycle ≤ 2%

^{2.} Essentially independent of operating temperature

■ TEST CIRCUITS AND WAVEFORMS

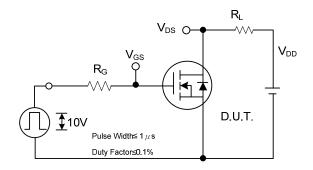


Peak Diode Recovery dv/dt Test Circuit

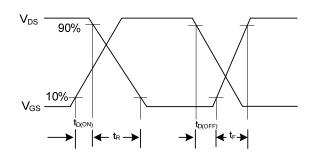


Peak Diode Recovery dv/dt Waveforms

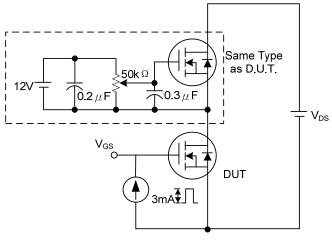
■ TEST CIRCUITS AND WAVEFORMS (Cont.)



Switching Test Circuit



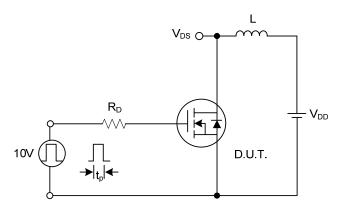
Switching Waveforms



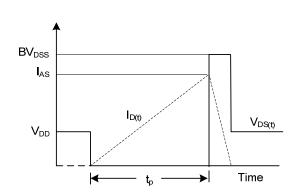
 V_{GS}

Gate Charge Test Circuit

Gate Charge Waveform

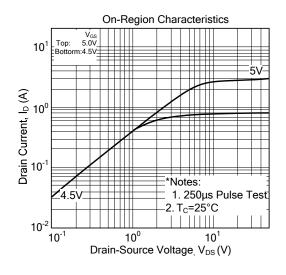


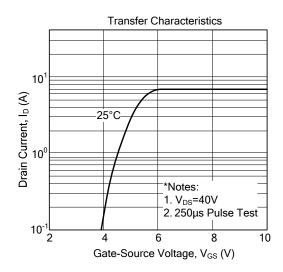
Unclamped Inductive Switching Test Circuit

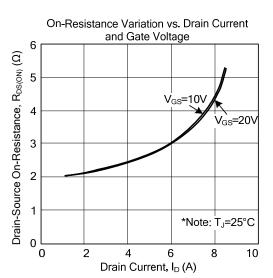


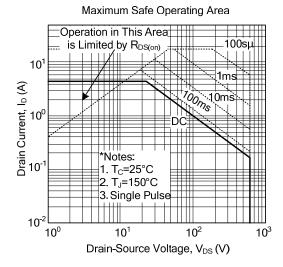
Unclamped Inductive Switching Waveforms

■ TYPICAL CHARACTERISTICS









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