



7N40

Preliminary

Power MOSFET

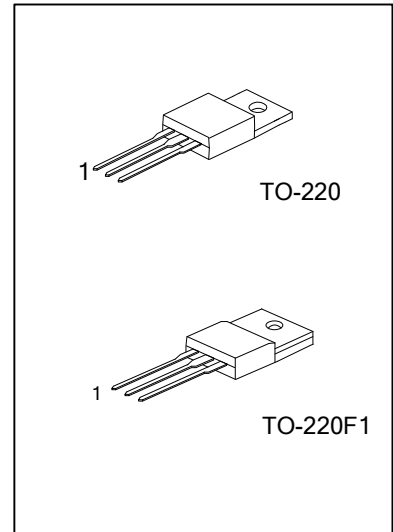
7 Amps, 400 Volts

N-CHANNEL POWER MOSFET

DESCRIPTION

The UTC **7N40** is an N-channel mode power MOSFET using UTC's advanced technology to provide customers with planar stripe and DMOS technology. This technology specializes in allowing a minimum on-state resistance and superior switching performance. It also can withstand high energy pulse in the avalanche and commutation mode.

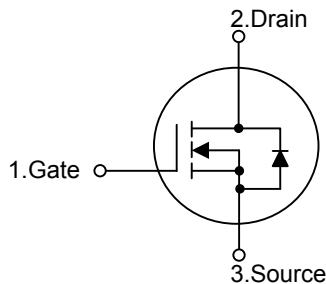
The UTC **7N40** is universally applied in electronic lamp ballast based on half bridge topology and high efficient switched mode power supply.



FEATURES

- * High switching speed
- * 7A, 400V, $R_{DS(ON)}=0.8\Omega$ @ $V_{GS}=10V$
- * 100% avalanche tested

SYMBOL



ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
7N40L-TA3-T	7N40G-TA3-T	TO-220	G	D	S	Tube
7N40L-TF1-T	7N40G-TF1-T	TO-220F1	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>7N40L-TA3-T</p> <p>(1)Packing Type</p> <p>(2)Package Type</p> <p>(3)Lead Free</p>	<p>(1) T: Tube</p> <p>(2) TA3: TO-220, TF1: TO-220F1</p> <p>(3) G: Halogen Free, L: Lead Free</p>
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■ ABSOLUTE MAXIMUM RATINGS ($T_C=25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DS}	400	V
Gate-Source Voltage		V_{GS}	± 30	V
Drain Current	Continuous ($T_C=25^\circ\text{C}$)	I_D	7	A
	Pulsed (Note 1)	I_{DM}	28	A
Avalanche Current (Note 1)		I_{AR}	7.0	A
Avalanche Energy	Single Pulsed (Note 2)	E_{AS}	360	mJ
	Repetitive (Note 1)	E_{AR}	9.8	mJ
Peak Diode Recovery dv/dt (Note 3)		dv/dt	4.5	V/ns
Power Dissipation	TO-220	P_D	98	W
	TO-220F1		39	W
Derate above 25°C	TO-220		0.78	W/ $^\circ\text{C}$
	TO-220F1		0.315	W/ $^\circ\text{C}$
Junction Temperature		T_J	+150	$^\circ\text{C}$
Storage Temperature Range		T_{STG}	-55~+150	$^\circ\text{C}$

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

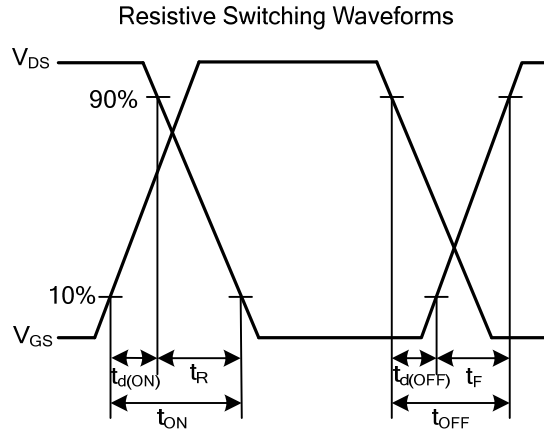
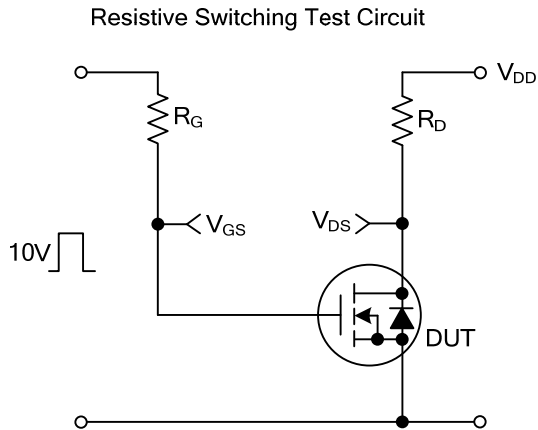
PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220	θ_{JA}	62.5	$^\circ\text{C/W}$
	TO-220F1		62.5	
Junction to Case	TO-220	θ_{JC}	1.28	$^\circ\text{C/W}$
	TO-220F1		3.2	

■ ELECTRICAL CHARACTERISTICS (T_c=25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	I _D =250μA, V _{GS} =0V	400			V
Breakdown Voltage Temperature Coefficient		ΔBV _{DSS} /ΔT _J	Reference to 25°C, I _D =250μA		0.43		V/°C
Drain-Source Leakage Current		I _{DSS}	V _{DS} =400V, V _{GS} =0V			1	μA
Gate- Source Leakage Current	Forward	I _{GSS}	V _{GS} =+30V, V _{DS} =0V			+100	nA
	Reverse		V _{GS} =-30V, V _{DS} =0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250μA	2.0		4.0	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =3.5A		0.62	0.8	Ω
DYNAMIC PARAMETERS							
Input Capacitance		C _{ISS}	V _{GS} =0V, V _{DS} =25V, f=1.0MHz		600	780	pF
Output Capacitance		C _{OSS}			105	135	pF
Reverse Transfer Capacitance		C _{RSS}			13	17	pF
SWITCHING PARAMETERS							
Total Gate Charge		Q _G	V _{GS} =10V, V _{DS} =320V, I _D =7A (Note 4, 5)		16.5	22	nC
Gate to Source Charge		Q _{GS}			4.5		nC
Gate to Drain Charge		Q _{GD}			8.5		nC
Turn-ON Delay Time		t _{D(ON)}	V _{DD} =200V, I _D =7A, R _G =25Ω (Note 4, 5)		20	50	ns
Rise Time		t _R			75	160	ns
Turn-OFF Delay Time		t _{D(OFF)}			35	80	ns
Fall-Time		t _F			50	110	ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous Current		I _S				7	A
Maximum Body-Diode Pulsed Current		I _{SM}				28	A
Drain-Source Diode Forward Voltage		V _{SD}	I _S =7A, V _{GS} =0V			1.5	V
Body Diode Reverse Recovery Time		t _{RR}	I _S =7A, V _{GS} =0V, di _F /dt=100A/μs		220		ns
Body Diode Reverse Recovery Charge		Q _{RR}	(Note 4)		1.3		μC

- Notes: 1. Repetitive Rating: Pulse width limited by maximum junction temperature
 2. L = 13mH, I_{AS} = 7A, V_{DD} = 50V, R_G = 25Ω, Starting T_J = 25°C
 3. I_{SD} ≤ 7A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C
 4. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%
 5. Essentially independent of operating temperature

■ TEST CIRCUITS AND WAVEFORMS



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